

**Listing and Amendments to the Claims**

This listing of claims will replace the claims that were published in the PCT Application:

1/ (original) Method of correcting the image distortions created on the screen of a cathode ray tube comprising the following steps:

- determining in digital form, the values of the line scan current synchronously and of the frame scan current asynchronously
- using these values to address the inputs of a correction memory
- programming the correction memory so that for each address at input there corresponds at output at least one correction value
- converting the correction value with the aid of a digital/analogue converter
- filtering the correction values with the aid of a low-pass filter
- applying an electrical quantity dependent on the correction value to at least one magnetic coil of the deflection system disposed on the cathode ray tube.

2/ (currently amended) Device for correcting the line and/or frame fields of a deflector for cathode ray tube comprising:

- a current sensor ~~(1)~~ for evaluating the value of the line current  $I_l$
- a series of comparators ~~(40 to 4N)~~ intended to compare the value of the line current  $I_l$  with reference values
- a current sensor ~~(2)~~ for evaluating the value of the frame current  $I_t$
- an analogue/digital converter ~~(31)~~ for converting the analogue value of the frame current
- a programmed correction memory ~~(50)~~ which is addressed by the output signals from the comparators and from the analogue/digital converter so as to deliver to at least one digital/analogue converter ~~(70, 71...7N)~~, data ~~(60, 61, ...6N)~~ which are dependent on the addressing signals
- a low-pass filter ~~(80, 81, ...8N)~~ for filtering the output of the digital/analogue converter

- at least one correction coil (~~100, 101, ... 10N~~) for correcting the deflection fields of the deflector, generating a correction field as a function of the output value from the low-pass filter.

3/ (currently amended) Correction device according to Claim 2, ~~characterized in that~~ wherein the sampling frequency of the analogue/digital converter (~~31~~) is at least equal to the line scan frequency of the deflector.

4/ (currently amended) Device according to Claim 2, ~~characterized in that~~ wherein the cut-off frequency of the filter (~~80, 81, ... 8N~~) is around 150 KHz.

5/ (currently amended) Device according to Claim 2, ~~characterized in that~~ wherein the digital analogue converters operate at a frequency of at least 350 KHz.